

CURRICULUM VITAE FOR MICHAEL QUINN

1. Personal Information

Michael J. Quinn
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2. Research Interests

Image Processing, Image and Video Compression, Data Fusion, Sensor Networks, Perception

3. Education

2002 – Present

University of California – Santa Barbara
Department of Electrical and Computer Engineering – Ph.D. Program
Passed ECE Ph.D. Screening Exam September 2003
GPA 3.86/4.0

1997-1999

University of Missouri – Rolla
MS in Electrical Engineering
Thesis: ‘Analysis of Diurnal Infrared Data for Landmine Detection’
GPA 3.75/4.0

1992-1997

University of Missouri – Rolla
BS in Electrical Engineering
GPA 3.4/4.0

4. Professional Experience

January 2001 - August 2002

Test Software Engineer

General Motors – General Assembly Engineering – Assembly Verification Group

Developed and maintained software for programming and verification of vehicle control modules during the assembly process. I was the software owner for five vehicle control modules. I worked closely with design engineers and plant support engineers to develop test requirements, process flow, and validation plans for software development. I assisted design engineers and suppliers with module troubleshooting.

In addition to software development, I supported the test software in several assembly plants. I was in charge of all troubleshooting and debugging for issues discovered during production.

July 1999 – December 2000

General Motors – College Graduate in Training

18-month program with assignments in different departments

Design Engineer – Completed Unigraphics V15 (3D modeling software) training. I worked as a design engineer in the truck chassis group.

Component Test Engineer – I executed functional tests on antilock braking system control modules. I was responsible for the creation of test plans based on specification. I also designed and oversaw the building of test equipment.

Vehicle Test Engineer – Executed electrical tests to validate new wiring harness in heavy-duty pickup truck. Developed test procedure from system schematics, assisted technicians in executing the procedure and fully documented results as a deliverable for the design engineer.

General Assembly Test Systems Engineer – I led a database consolidation project for simplification of test development and vehicle validation operations. This resulted in considerable time savings due to the elimination of manual data entry. Also wrote EnC (Entertainment and Comfort) protocol translation program to accelerate analysis of test output data.

Assembly Process Validation Engineer – I led capability study of 30+ multi-spindle tools which were to be used in a new assembly plant. This study led to the development of static torque specifications.

Statistical Engineer – I led and assisted with several projects aimed to improve vehicle quality by utilizing Shainin LLC Red X Strategies. I worked with suppliers, design engineers, and plant personnel to determine root cause of quality issues. Once identified, the problem was reduced or eliminated via product and procedure changes.

5. Academic / Research Experience

January 1997-May 1999

Graduate Research Assistant

UMR Intelligent Systems Center

I worked on the UMR-ISC project entitled ‘Multisensor Data Fusion for Ground-Based Landmine Remediation.’ At the beginning of the project, I performed extensive research into current landmine detection research. I later looked at data fusion techniques and their potential application to landmine detection. In addition, I performed some analysis on Department of Defense image data (Infrared, Ground Penetrating Radar) in various formats (SEG2, ATR).

For my thesis, I examined a thermodynamic model of a buried landmine. I then explored the feasibility of landmine detection using infrared imagery taken over a 24-hour period.

August 1997-December 1998

Graduate Teaching Assistant

UMR Department of Electrical and Computer Engineering

I taught the laboratory portion of Introduction to Digital Electronics to undergraduates for 3 semesters and one summer. During the summer of 1998, I also taught the lecture portion of the class. In the laboratory, we worked with Mentor Graphics and Xilinx FPGA's to design and test logic circuits.

6. Professional Associations

Institute of Electrical and Electronic Engineers, Sigma Xi Scientific Research Society,
SPIE – The International Society for Optical Engineering